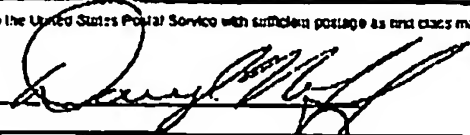


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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE	
In re application of: <b>Huajie Chen et al.</b>	Date: <b>February 8, 2005</b>
Serial Number: <b>10/605,134</b>	Examiner: <b>Fernando L. Toledo</b>
Filed: <b>September 10, 2003</b>	Group Art Unit: <b>2823</b>
Title: <b>STRUCTURE AND METHOD OF MAKING STRAINED CHANNEL CMOS TRANSISTORS HAVING LATTICE-MISMATCHED EPITAXIAL EXTENSION AND SOURCE AND DRAIN REGIONS</b>	H. Daniel Schnurmann IBM Corporation D/18G, B/300, Zip 482 2070 Route 52 Hopewell Junction, NY 12533-6531

**AMENDMENT UNDER 37 C.F.R. §1.312****RECEIVED  
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Sir:

Subsequent to the Notice of Allowance dated November 12, 2004, and prior to this application being issued, applicants submit the following amendments and remarks for entry in the application.

FIS920030198US1

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